



PTO/SB/08a/b (08-03)

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Substitute for form 1449A/B/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)				Complete if Known	
				Application Number	10/777,804
				Filing Date	February 12, 2004
				First Named Inventor	Masud Beroz
				Art Unit	2826
				Examiner Name	Not Yet Assigned Kevin Quinto
Sheet	1	of	1	Attorney Docket Number	TESSERA 3.0-236 DIV

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)			
12	AA	US-5,148,265	09/1992	Khandros et al.	
	AB	US-5,455,390	10/1995	DiStefano et al.	
	AC	US-5,659,952	08/1997	Kovac et al.	
12	AD	US-5,830,783	11/1998	Aiello et al.	

FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		Country Code ³ -Number ⁴ -Kind Code ⁵ (if known)	MM-DD-YYYY			

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¹ Applicant's unique citation designation number (optional). ² Applicant is to place a check mark here if English language Translation is attached.
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Examiner Signature	Kevin Quinto	Date Considered	8/11/05
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				Filing Date	February 12, 2004
				First Named Inventor	Masud Beroz
				Art Unit	2826
				Examiner Name	Not Yet Assigned — Kevin Quisto
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<i>KQ</i>	CA	DOAME and FRANZON, Multi-Chip Module Technologies and Alternatives; the Basics (eds., 1993, pp. 450-476).			

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)				Application Number	Not Yet Assigned
				Filing Date	Concurrently Herewith
				First Named Inventor	Masud Beroz
				Art Unit	<i>DNA 2826</i>
				Examiner Name	Not Yet Assigned <i>Kevin Quinto</i>
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<i>22</i>	AA	US-5,148,266	9/15/92	Khandros et al.	
	AB	US-5,915,752	6/29/99	DiStefano et al.	
	AC	US-5,518,964	5/21/96	DiStefano et al.	
	AD	US-4,455,390	10/3/95	DiStefano et al.	
	AE	US-5,798,286	8/25/98	Faraci et al.	
	AF	US-4,875,617	10/24/89	Citowsky	
	AG	US-5,763,941	6/9/98	Fjelstad	
	AH	US-5,629,239	5/13/97	DiStefano et al.	
	AI	US-5,821,609	10/13/98	DiStefano et al.	
	AJ	US-5,976,913	11/2/99	Distefano	
	AK	US-5,148,266	9/15/92	Khandros et al.	
	AL	US-5,915,752	6/29/99	DiStefano et al.	
	AM	US-5,518,964	5/21/96	DiStefano et al.	
	AN	US-4,455,390	10/3/95	DiStefano et al.	
	AO	US-4,818,728	04/1989	Rai et al.	
<i>22</i>	AP	US-6,288,559	09/2001	Bernier et al.	
	AQ	US-6,342,443	01/2002	Tao et al.	

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	BA					

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<i>22</i>	CA	Multi-Chip Module Technologies and Alternatives; the Basics (Doame and Franzon, eds., 1993, pp. 450-476).	
<i>22</i>	CB	Harper, "Electronic Packaging and Interconnection Handbook", McGraw-Hill, Section 5.2, 1991	

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